

## PRESENTER BIO:

**Lech Jóźwiak** received his M. Sc. Eng. degree in Electronics in 1976 and Ph. D. degree in Technical Sciences (Specialty Computer Systems and Automatic Control) in 1982, both from the Faculty of Electronics, Warsaw University of Technology, Warsaw, Poland. From 1976 till now he has been continuously working in the area of his specialization in academia, research institutes or industry. He has an international work experience from Poland, The Netherlands, USA, Canada and Australia. Currently he is an Associate Professor, Head of the Section of Digital Circuits and Formal Design Methods, in the Departments of Electronic Systems, Faculty of Electrical Engineering, Eindhoven University of Technology, The Netherlands.

He is an author of a paradigm of life-inspired systems, methodology of quality-driven design of electronic systems, information-driven approach to digital circuit synthesis, and theories of information relationships and measures, and general decomposition of discrete relations, that have both a theoretical value and considerable practical importance. He is also a creator of several practical products in the fields of application-specific embedded systems and EDA tools. His current research interests include: system, circuit, information and design theories and technologies; decision theory; artificial intelligence; embedded and high-performance systems; (heterogeneous) processor, memory and communication architectures; re-configurable and (massively) parallel computing; dependable computing; multi-objective circuit and system optimization; system analysis and validation; and technology development and transfer. He is an author of more than 180 journal and conference papers, some book chapters, and presenter of several keynotes and numerous tutorials at international conferences and summer schools.

He is an Editor in Chief of the journal of “Microprocessors and Microsystems”, and Editor of “Journal of Systems Architecture” and “International Journal of High Performance Systems Architecture”. He is a Director of EUROMICRO; Founder and Steering Committee Chair of the EUROMICRO Symposium on Digital System Design; Advisory Committee and Organizing Committee member in the IEEE International Symposium on Quality Electronic Design; and program committee member of many other conferences.

He is or was an advisor and consultant to the industry, Ministry of Economy and Commission of the European Communities (a. o. he advised the European Commission in relation to Embedded and High-performance Computing Systems for the purpose of the Framework Program 7).

He was a recipient of multiple Letters of Recognition and Thanks for highly esteemed services and Recognition Diploma for exceptional performance and achievements a. o. from the Minister of Economy of Poland, presidents of professional societies and organizations (e.g. EUROMICRO, IEEE, ISQED, MANT, HiPEAC) and chief editors of international scientific journals (e.g. IEEE Transactions on Computers; IEEE Transactions on CAD; Journal of Systems Architecture; Microprocessors and Microsystems, Integration: the VLSI Journal; IEE Proceedings: Computers and Digital Techniques; Electronic Letters). In 2008 he was a recipient of the Honorary Fellow Award of the International Society of Quality Electronic Design for “Outstanding Achievements and Contributions to Quality of Electronic Design”. His biography is listed in “The Roll of Honour of the Polish Science” of the Polish State Committee for Scientific Research and in Marquis “Who is Who in the World” and “Who is Who in Science and Technology”.