



17th Symposium on

DDECS
DESIGN & DIAGNOSTICS of ELECTRONIC CIRCUITS & SYSTEMS

April 23-25, 2014, Warsaw, Poland

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Call for Papers

The IEEE Symposium on Design and Diagnostics of Electronic Circuits and Systems provides a forum for exchanging ideas, discussing research results, and presenting practical applications in the areas of design, test, and diagnosis of electronic circuits and systems.

The DDECS Symposium series has been organized by these European countries: Czech Republic (1997, 2002, 2006, 2009, 2013), Poland (1998, 2003, 2007), Slovakia (2000, 2004, 2008), Hungary (2001, 2005), Austria (2010), Germany (2011) and Estonia (2012).

DDECS 2014 will take place in Warsaw, the home town of such celebrities as Marie Skłodowska-Curie and Frédéric Chopin. With its 78 universities, Warsaw hosts 270 thousand students. Over 700 thousand foreign tourists are attracted every year by Warsaw's cultural events of international renown (like National Theatres' Meeting and Jazz Jamboree), museums (including the National Museum and the Warsaw Rising Museum), and the Copernicus Science Centre promoting various domains of science with hundreds of its interactive exhibits.

The Symposium is organized by the Institute of Microelectronics & Optoelectronics – Warsaw University of Technology, and sponsored by the Test Technology Technical Council (TTTC) of the IEEE Computer Society.

Topics of interest include but are not limited to:

SoC and NoC Design and Test
ASIC/FPGA Design
Built-in Self-Test and Self-Repair
Bio-Inspired Hardware
Design Verification/Validation
Formal Methods in System Design
Hardware/Software Co-Design
IP-based Design
Logic Synthesis
Defect/Fault Tolerance and Reliability

Design and Test in Nano-Technologies
Analog, Mixed-Signal, RF Design and Test
ATE Hardware and Software
Design for Testability and Diagnosis
On-line Testing
Embedded Systems Testing
Memory, Processor Testing
MEMS Testing
Physical Design
Dependable HW/SW Systems

Submissions:

Prospective authors are cordially invited to submit original papers using the electronic submission system at the Symposium web page. Papers in English with a length of 6 pages maximum in IEEE conference style are expected. Specialized student and industrial sessions, as well as embedded tutorials, will be organized at the symposium. Accepted papers will be included in the Symposium Proceedings and submitted for inclusion into IEEE Xplore as well as other Abstracting and Indexing (A&I) databases.

Important Dates:

Submission deadline: **January 19th, 2014 (extended)**
Notification of acceptance: **March 3rd, 2014**
Camera-ready papers: **March 16th, 2014**

Further Information:

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